

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Keiichiro KATA & Shinichi CHIKAKI

Title: **PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR WAFER**

Serial No.: Not Yet Assigned

Application for reissue of U.S. Patent No. 5,844,304, granted Dec. 1, 1998

Filed: Herewith

Examiner: Unassigned

Art Unit: Unassigned

PRELIMINARY AMENDMENT AND 37 CFR § 1.173(c) STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Amendments to the Claims are reflected in the listing which begins on page 2 of this document.

Remarks/Arguments begin on page 7 of this document.

Please amend the application as follows: